

Fig. 1

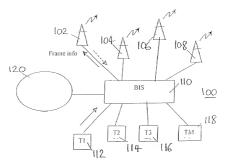


Fig. 2

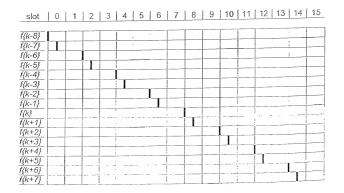


Fig. 3



Fig. 4

ID packet	CA <b>lah</b> data			
Slot boundary		Ha	If-slot boundary	

## Fig. 5

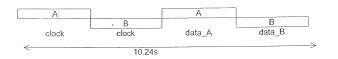


Fig. 6

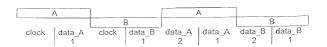


Fig. 7